

TEQIP CELL
JADAVPUR UNIVERSITY

CORRIGENDUM-I


[To the NCB-Bid Document for the supply of Software: Wind River Work bench 3.2 tools for Vx Works 6.8 (includes Vx Works Binaries). Perpetual : Node locked Vx Works 6.8 OEM license ICE Hardware Emulator) :: TEQIP-II/WB/ WB2G02/86]

Following corrections/changes are done in the specification as suggested by the department (Updated version of the software) in the amendments issued after Pre-bid Conference held on 22nd July 2013 & duly endorsed by the SPFU, West Bengal.

Subject	Previous version and specification			Current version and specification		
Part 1	Sl. No.	Product Name & Description	User	Sl.No	Product Name & Description	User
	1	Wind River Workbench3.2 tools for for VxWorks 6.8 [Includes Vx works binaries], Perpetual	3	1	Wind River Workbench 3.3 tools for VxWorks 6.9[Includes Vx works binaries], Perpetual	3
	Sl.No	Product Name & Description	User	Sl.No	Product Name & Description	User
	2	<p><i>Target Board (Power QUICC II 8349 processor based)</i> <i>Reference Board to work with VxWorks RTOS</i> Specification: Core processor o MPC8347E 667Mhz or MPC8349E 667Mhz processor Available memory o 256MB DDR SDRAM SODIMM o 8MB on-board flash o 8KB EEPROM o 128MB local bus SDRAM DIMM o 256K-bit I2C serial EEPROM Support for the following PowerQUICC II Pro peripherals: o PCI-X controller: 3.3v 64-bit PCI bus up to 133MHz o Memory controller: up to DDR333 memory bus o Interrupt controller and timers o (2) 10/100/1000BaseT internal Ethernet MAC</p>	2	2	<p>Target Board featuring the XC7Z020 CS484-1 EPP</p> <ul style="list-style-type: none"> • Cable & Power Supply • USB Flash Drive • the XC7Z020-CS484-1 Power 12V configuration • Onboard configuration circuitry • 16MB Quad SPI Flash • SDIO Card Interface (boot) • PC4 and 20 pin JTAG ports <p>Memory</p> <ul style="list-style-type: none"> • DDR3 Component Memory 128 MB • Support 32 data width • 16MB Quad SPI Flash • IIC - 1 KB EEPROM <p>Communication & Networking</p> <ul style="list-style-type: none"> • GigE RGMII Ethernet (PS) • USB OTG 1 (PS) - Host USB • IIC Bus Headers/HUB (PS) • 1 CAN with Wake on CAN (PS) • USB UART (PS) 	2

<p>interfaces using RJ45 connectors</p> <ul style="list-style-type: none"> o (2) RS-232 interfaces using Mini DB9 connectors o USB Host Mini A/B port <p>Hard reset via a momentary switch Seven-segment display on CPU GPIO User switches on CPU GPIO 16-pin and 52-pin JTAG debug connections</p>	<p>Video/ Display</p> <ul style="list-style-type: none"> • HDMI Video OUT • 8X LEDs <p>Clocking</p> <ul style="list-style-type: none"> • 200MHz Fixed PL Oscillator (Differential LVDS) • 156.25MHz (default) I2C Programmable Oscillator (Differential LVDS) • 33.33MHz Fixed PS System Oscillator (Single-Ended CMOS) <p>Control & I/O</p> <ul style="list-style-type: none"> • 3 User Push Buttons • 2 User Switches • 8 User LEDs <p>Power</p> <ul style="list-style-type: none"> • 12V wall adapter or ATX • Voltage and Current measurement capability of supplies
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Date: 22.07.2013


22/07/13
Sanjib Acharyya,
Nodal Officer &
Head of the Committee (offg.)
